

FRAUNHOFER INSTITUTE FOR ORGANIC ELECTRONICS, ELECTRON BEAM AND PLASMA TECHNOLOGY FEP





- 1 Bidirectional microdisplay
- 2 Evaluation kit

Fraunhofer Institute for Organic Electronics, Electron Beam and Plasma Technology FEP

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BIDIRECTIONAL SVGA MICRODISPLAY EBCW1020A

Introduction

Mobile personal electronics have become tremendously popular in the last few years and further developments can at best be guessed today. Smart phones and tablets are the prime examples, having nearly overturned all traditional rules with the mobile generation, processing and consumption of data in the broadest sense.

Notwithstanding all developments, solutions to one elementary problem leave lots of room for improvement: Even though screens have gotten bigger, the amount of information that can be displayed is limited for ergonomic reasons. Neither an unlimited growth in the diagonal screen size is possible, nor the unlimited shrinking of content. A mobile multimedia interface is therefore lacking. This can be made possible by video or data goggles, which support the discreet display of information even in public spaces. Initial practical implementations of this technology are available.

High-resolution miniaturized displays are an essential component of these goggles. Thanks to tremendous pixel densities, they are able to generate high-resolution virtual images with a large viewing angle. The display with SVGA resolution described here is based on OLED-on-silicon technology, and is therefore able to generate high-contrast images with a large color space while consuming minimal energy. Unlike solutions that are already commercially available, the display has an embedded SVGA image sensor which can be used for example for interaction with the user (e.g. eye tracking by capturing the center of the pupil).



Technical description

A bidirectional display is able to both reproduce and record images. This is made possible by a special pixel arrangement. A pixel therefore not only comprises 4 subpixels for image rendering (RGBW) but also a photo diode for light detection. This structure results in a pixel matrix consisting of two sub-matrices: a display matrix with a nested image sensor matrix.

The bidirectional microdisplay described here has a resolution of 800 x 600 pixels (SVGA). The data is provided to the display via a 24-bit (R, G, B, 8-bit each) parallel interface. The value of the white pixel can either be determined through an internal calculation or provided externally via an additional 8-bit channel. Data synchronization is realized by additional signals: VS (vertical sync), HS (horizontal sync) and DE (data enable). The grayscale camera output (8-bit) is realized over a similar parallel interface.

The microdisplay chip also has a two-wire-interface (TWI) for configuration. This interface is used to adjust the brightness of the display, the camera exposure time and the time sequence control of the camera and display. The microdisplay supports both the time sequential and parallel operation of OLED emission and image sensor detection.

A development system is available for evaluation to make it easier for users to adopt this new generation of bidirectional microdisplays. It allows to operate the microdisplay on a standard HDMI interface. The power supply and the image sensor data are provided over a standard USB3.0 interface.

Parameters	
Display resolution	800 × 600
Active area	12.8 mm × 9.6 mm
Display diagonal	0.6"
Pixel setup	RGBW + photo diode
Pixel pitch	16 μm × 16 μm
Color depth	24-bit
Display interface	24-bit RGB digital, parallel + synchronization signals CLK, HS, VS and DE
Display brightness	250 cd/m² (typ.)
Camera resolution	800 × 600
Camera interface	8-bit grayscale digital, parallel + synchronization signals CLK, HS, VS and DE
Configuration interface	TWI (two-wire-interface)
I/O voltage	1.6V 5.5V
Core voltage	1.6 V 2.0 V
Temperature range	-20°C − +65°C

References:

BMBF 16SV3682 MAVO 823 279



3 Chip photo of bidirectional
OLED microdisplay
4 Detailed image of nested
display and camera matrix
5 Raw image of the image sensor,
parallel operation of the display
and sensor matrix, image sensor
captures display content

0.18 µm

CMOS technology